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Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 2065)

Friday 13 November 2009

Laboratory Session: ICTP RVI Platform. A Design Example. - Adriatico Guest House
Informatics Lab. (17:00-18:00)

time	title	presenter
17:00	Laboratory Session: ICTP RVI Platform. A Design Example.	K.M. KHARE