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# Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 2065)

## Tuesday 27 October 2009

**Synthesis II - Introduction to VHDL - Adriatico Guest House Kastler Lecture Hall**

**(14:30-15:30)**

time	title	presenter
14:30	Synthesis II - Introduction to VHDL	N. ABDALLAH