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Advanced Training Course on FPGA Design and VHDL for Hardware Simulation and Synthesis | (smr 2065)

Tuesday 27 October 2009

**(LiberioTM IDE) Functional Simulation. Synthesis - Adriatico Guest House Kastler Lecture
Hall (17:00-18:00)**

time	title	presenter
17:00	(LiberioTM IDE) Functional Simulation. Synthesis	N. ABDALLAH