

Table of contents

Wednesday 31 March 2010	1
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ICTP Latin-American Basic Course on FPGA Design for Scientific Instrumentation | (smr 2177)

Wednesday 31 March 2010

Digital Signal Processing with FPGA. A Design Example using the μ Lab Virtual Panel (constant multiplier and level shifter). (10:00-11:00)

time	title	presenter
10:00	Digital Signal Processing with FPGA. A Design Example using the μ Lab Virtual Panel (constant multiplier and level shifter).	ANDRES AIRABELLA