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# ICTP Latin-American Basic Course on FPGA Design for Scientific Instrumentation | (smr 2177)

## Wednesday 31 March 2010

**Digital Signal Processing with FPGA. A Design Example using the  $\mu$ Lab Virtual Panel (constant multiplier and level shifter) (contd). (11:30-12:30)**

time	title	presenter
11:30	Digital Signal Processing with FPGA. A Design Example using the $\mu$ Lab Virtual Panel (constant multiplier and level shifter) (contd).	