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Asian Regional School on FPGA Design for Scientific Instrumentation | (smr 2689)

Wednesday 16 December 2015

- (08:30-20:00)

time	title	presenter
08:30	Advanced SOC-FPGA trends	
10:30	Coffee Break	
11:00	FPGA Debugging	ANDRES CICUTTIN
12:00	Lunch	
13:30	Advanced SOC trends	PAUL BECKETT
15:30	Coffee Break	
16:00	Digital Signal Processing in FPGA (Lab. Tutorial)	AAMIR IRSHAD
18:00	Dinner	