

Table of contents

Wednesday 09 December 2015	1
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Asian Regional School on FPGA Design for Scientific Instrumentation | (smr 2689)

Wednesday 09 December 2015

- (08:30-20:00)

time	title	presenter
08:30	CMOS Technology and VLSI Design	SANDRO BONACINI
10:30	Coffee Break	
11:00	Evolution of Reconfigurable Devices	YOUSAF ZAFAR
12:00	Lunch	
13:30	Fundamentals on Digital Signal Processing	WANOD KUMAR
15:30	Coffee Break	
16:00	VHDL Design	SISTERNA CRISTIAN ALEJANDRO
18:00	Dinner	