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Joint ICTP-IAEA School on Hybrid Reconfigurable Devices for Scientific Instrumentation | (smr 2737)

Thursday 04 June 2015

- LB (Euler Lecture Hall) (08:30-19:30)

| time | title | presenter |
|-------|---|------------------------------------|
| 08:30 | Project 1 Real-time Data acquisition and Ethernet on ZYNQ FPGA | H. RONGEN,A. CICUTTIN, C. SISTERNA |
| 10:00 | Project 2 Digital Pulse Processor for Nuclear Spectroscopy | M. BOGOVAC, I. DARBY, I. KANIU |
| 10:00 | Coffee Break | |
| 10:20 | Project 3 Software application on Linux with hardware accelerator | F. RINCON, M.L. CRESPO, J. DONDO |
| 10:20 | Project 1 Real-time Data acquisition and Ethernet on ZYNQ FPGA | H. RONGEN,A. CICUTTIN, C. SISTERNA |
| 12:00 | Project 2 Digital Pulse Processor for Nuclear Spectroscopy | M. BOGOVAC, I. DARBY, I. KANIU |
| 12:20 | Project 3 Software application on Linux with hardware accelerator | F. RINCON, M.L. CRESPO, J. DONDO |
| 12:40 | Lunch Break | |
| 13:30 | Project 1 Real-time Data acquisition and Ethernet on ZYNQ FPGA | H. RONGEN,A. CICUTTIN, C. SISTERNA |
| 15:00 | Project 2 Digital Pulse Processor for Nuclear Spectroscopy | M. BOGOVAC, I. DARBY, I. KANIU |
| 15:20 | Project 3 Software application on Linux with hardware accelerator | F. RINCON, M.L. CRESPO, J. DONDO |
| 15:20 | Project 1 Real-time Data acquisition and Ethernet on ZYNQ FPGA | H. RONGEN,A. CICUTTIN, C. SISTERNA |
| 15:40 | Coffee Break | |
| 17:00 | Project 2 Digital Pulse Processor for Nuclear Spectroscopy | M. BOGOVAC, I. DARBY, I. KANIU |
| 17:20 | Project 3 Software application on Linux with hardware accelerator | F. RINCON, M.L. CRESPO, J. DONDO |
| 19:00 | Participant Presentations | |