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Joint ICTP-IAEA School on Systems-on-Chip Based on FPGA for Scientific Instrumentation and Reconfigurable Computing | (smr 3891)

Monday 20 November 2023

- Informatics Laboratory (AGH) (09:00-18:30)

time	title	presenter
09:00	Registration formalities	
09:30	Opening Remarks	MARIA LIZ CRESPO (ICTP, ITALY), ANDRES CICUTTIN (ICTP, ITALY)
09:45	School Outline	MARIA LIZ CRESPO
10:00	Brief introduction from participants	
10:15	Coffee Break	
10:45	FPGA and System-on-Chip (SoC) technology	CRISTIAN ALEJANDRO SISTERNA
12:30	Lunch Break	
14:00	SoC Architecture and Design Methodology	CRISTIAN ALEJANDRO SISTERNA
16:00	Coffee Break	
16:30	SoC Architecture and Design Methodology	CRISTIAN ALEJANDRO SISTERNA
17:30	Use of VMs and User environment	LUIS GUILLERMO GARCÍA ORDÓÑEZ