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Joint ICTP-IAEA School on Systems-on-Chip Based on FPGA for Scientific Instrumentation and Reconfigurable Computing | (smr 3891)

Tuesday 21 November 2023

- Adriatico Guest House - Informatics Laboratory (09:00-18:30)

time	title	presenter
09:00	Zynq SoC: Evaluation and Development Board	CRISTIAN ALEJANDRO SISTERNA
10:00	Coffee Break	
10:30	C for Embedded Systems	CRISTIAN ALEJANDRO SISTERNA
12:30	Lunch Break	
14:00	Hardware and Software Interrupts on SoC	CRISTIAN ALEJANDRO SISTERNA
15:00	AXI Bus and Custom IP Cores	CRISTIAN ALEJANDRO SISTERNA
16:00	Coffee Break	
16:30	Guide for Lab 1: Hello World + GPIO In/Out	ROMINA SOLEDAD MOLINA
16:45	Lab 1: Hello World + GPIO In/Out	LAB TUTORS