## Joint ICTP-IAEA School on Systems-on-Chip Based on FPGA for Scientific Instrumentation and Reconfigurable Computing | (smr 3891)

Contribution ID: 114 Type: not specified

## Lab 2: ComBlock + RTL instantiation

Wednesday, 22 November 2023 15:15 (0:45)

Content

Summary

Presenter(s): LAB TUTORS

Session Classification: not yet classified