Joint ICTP-IAEA School on Systems-on-Chip Based on FPGA for Scientific Instrumentation and Reconfigurable Computing | (smr 3891)

Contribution ID: 119 Type: not specified

Guide for Lab 3: SoC-FPGA Development Framework: UDMA & Jupyter Notebook

Thursday, 23 November 2023 16:00 (0:15)

Content

Summary

 $Presenter(s): \quad {\rm WERNER\ FLORIAN\ (ICTP,\ Italy)}$

Session Classification: not yet classified