## Joint ICTP-IAEA School on Systems-on-Chip Based on FPGA for Scientific Instrumentation and Reconfigurable Computing | (smr 3891)

Contribution ID : 120 Type : not specified

## Lab 3: SoC-FPGA Development Framework: UDMA & Jupyter Notebook

Thursday, 23 November 2023 16:15 (2:15)

Content

Summary

Presenter(s): LAB TUTORS

 $\textbf{Session Classification}: \quad \mathsf{not} \ \mathsf{yet} \ \mathsf{classified}$