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Joint ICTP-IAEA School on Systems-on-Chip Based on FPGA for Scientific Instrumentation and Reconfigurable Computing | (smr 3891)

Thursday 23 November 2023

- Informatics Laboratory (AGH) (09:00-18:30)

time	title	presenter
09:00	The Role of Programmable Hardware Acceleration in Virtualized Communications Networks	LUCA VALCARENGHI
10:00	Coffee Break	
10:30	Real Time Operating System (FreeRTOS)	HEINRICH BARTHOLOMAUS RONGEN
12:30	Lunch Break	
14:00	FreeRTOS Tutorial	HEINRICH BARTHOLOMAUS RONGEN
15:30	Coffee Break	
16:00	Guide for Lab 3: SoC-FPGA Development Framework: UDMA & Jupyter Notebook	WERNER FLORIAN
16:15	Lab 3: SoC-FPGA Development Framework: UDMA & Jupyter Notebook	LAB TUTORS