Joint ICTP-IAEA School on Systems-on-Chip Based on FPGA for Scientific Instrumentation and Reconfigurable Computing | (smr 3891)

Contribution ID : 124 Type : not specified

Guide for Lab 4: SoC-FPGA DAQ system and TCL

Friday, 24 November 2023 15:00 (0:15)

Content

Summary

Presenter(s) : IVAN MORALES ARGUETA (ICTP, Italy)

Session Classification: not yet classified