

Table of contents

Friday 24 November 2023	1
-------------------------------	---

Joint ICTP-IAEA School on Systems-on-Chip Based on FPGA for Scientific Instrumentation and Reconfigurable Computing | (smr 3891)

Friday 24 November 2023

- Informatics Laboratory (AGH) (09:00-18:30)

time	title	presenter
09:00	The Open Standard RISC-V Architecture	FERNANDO RINCON CALLE
10:00	Coffee Break	
10:30	High Level Synthesis (HLS)	FERNANDO RINCON CALLE
12:30	Lunch Break	
14:00	HLS Demo	FERNANDO RINCON CALLE
15:00	Guide for Lab 4: SoC-FPGA DAQ system and TCL	IVAN MORALES ARGUETA
15:15	Lab 4: SoC-FPGA DAQ system and TCL	LAB TUTORS
16:00	Coffee Break	
16:30	Lab activities with tutors	