Joint ICTP-IAEA School on Systems-on-Chip Based on FPGA for Scientific Instrumentation and Reconfigurable Computing | (smr 3891)

Contribution ID : 136 Type : not specified

Project Lab 1.2: Digital Pulse Processing

Tuesday, 28 November 2023 14:00 (1:00)

Content

Summary

Presenter(s): MLADEN BOGOVAC (IAEA, Austria)

Session Classification: not yet classified