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Joint ICTP-IAEA School on Systems-on-Chip Based on FPGA for Scientific Instrumentation and Reconfigurable Computing | (smr 3891)

Tuesday 28 November 2023

- Informatics Laboratory (AGH) (09:00-18:30)

time	title	presenter
09:00	The European Spallation Source Data Acquisition System	RICHARD HALL-WILTON
10:00	Coffee Break	
10:30	Switch Architectures for Data Center Networks	PIERO CASTOLDI
11:30	Semiconductor Detectors, Fabrication Techniques, Quantum Detector Developments	RICHARD HALL-WILTON
12:30	Group Photo	
12:35	Lunch Break	
14:00	Project Lab 1.2: Digital Pulse Processing	MLADEN BOGOVAC
15:00	Guide for Project Lab 1.2: Digital Pulse Processing	IVAN MORALES ARGUETA
15:15	Lab activities with tutors	LAB TUTORS
16:00	Coffee Break	
16:30	Lab activities with tutors	