Joint ICTP-IAEA School on Systems-on-Chip Based on FPGA for Scientific Instrumentation and Reconfigurable Computing | (smr 3891)

Contribution ID: 143 Type: not specified

Project Lab 1.3: Baseline Restorer and Pile-Up Rejection

Wednesday, 29 November 2023 14:00 (1:00)

Content

Summary

Presenter(s): MLADEN BOGOVAC (IAEA, Austria)

Session Classification : not yet classified