



# Table of contents

Wednesday 29 November 2023 .....	1
----------------------------------	---

# Joint ICTP-IAEA School on Systems-on-Chip Based on FPGA for Scientific Instrumentation and Reconfigurable Computing | (smr 3891)

## Wednesday 29 November 2023

### - Informatics Laboratory (AGH) (09:00-18:30)

time	title	presenter
09:00	FPGAs computing just right thanks to application-specific arithmetic	FLORENT MARIE PAUL DUPONT DE DINECHIN
10:00	Coffee Break	
10:30	FPGAs computing just right thanks to application-specific arithmetic	FLORENT MARIE PAUL DUPONT DE DINECHIN
11:30	HyperFPGA: Experimental Infrastructure for Reconfigurable Supercomputing	WERNER FLORIAN
12:30	Lunch Break	
14:00	Project Lab 1.3: Baseline Restorer and Pile-Up Rejection	MLADEN BOGOVAC
15:00	Lab activities with tutors	LAB TUTORS
16:00	Coffee Break	
16:30	Lab activities with tutors	LAB TUTORS