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Joint ICTP-IAEA School on Systems-on-Chip Based on FPGA for Scientific Instrumentation and Reconfigurable Computing | (smr 3891)

Thursday 30 November 2023

- Informatics Laboratory (AGH) (09:00-18:30)

time	title	presenter
09:00	Academic Writing Strategy for Impacted Journal	MD MAMUN BIN IBNE REAZ
10:00	Coffee Break	
10:30	The FloPoCo arithmetic core generator	FLORENT MARIE PAUL DUPONT DE DINECHIN
11:30	Project 2: Digital Pulse Processing for X-ray Photon Detection and Energy Measurement	ANDRES CICUTTIN
12:30	Lunch Break	
14:00	Guide for Project Lab 2: Digital Pulse Processor (DPP)	BRUNO VALINOTI
14:30	Lab activities with tutors	LAB TUTORS
16:00	Coffee Break	
16:30	Lab activities with tutors	LAB TUTORS