Workshop on Fully-Programmable Systems-on-Chip for Scientific Applications | (smr 3983)

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ML and SoC-FPGA: Integration with a hardware design synthesis tool for ML

Wednesday, 30 October 2024 13:30 (0:30)

Content

Summary

Presenter(s): ROMINA MOLINA (ICTP, Italy)

Session Classification: Paralell lab session (Lab 1)