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Workshop on Fully-Programmable Systems-on-Chip for Scientific Applications | (smr 3983)

Wednesday 30 October 2024

Paralell lab session (Lab 1) - (11:00-17:00)

time	title	presenter
11:00	ML and SoC-FPGA: Training and Compression	ROMINA MOLINA
11:30	Project 1 with tutors	LECTURERS AND LAB TUTORS
12:30	Lunch Break	
13:30	ML and SoC-FPGA: Integration with a hardware design synthesis tool for ML	ROMINA MOLINA
14:00	Project 1 with tutors	LECTURERS AND LAB TUTORS
15:30	Tea/Coffee-Break	
15:50	Project 1 with tutors	