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Joint ICTP-IAEA School on Detector Signal Processing and Machine Learning for Scientific Instrumentation and Reconfigurable Computing | (smr 4110)

Monday 27 October 2025

- Kastler Lecture Hall (AGH) (09:00-18:00)

time	title	presenter
09:00	Opening Remarks	MARIA LIZ CRESPO
09:10	School Outline	MARIA LIZ CRESPO
09:30	Programmable System on Chip (PSoC) Architecture	CRISTIAN SISTERNA
10:30	Coffee Break	
11:00	Edge AI for the Internet of Things: Insights from Real-World Projects	DEJAN VUKOBRATOVIC
11:45	Development Board: Zynq SoC	CRISTIAN SISTERNA
12:30	Lunch Break	
14:00	C for Embedded Systems	CRISTIAN SISTERNA
15:00	Virtual Machine and User Environment	MAYNOR BALLINA
15:15	Guide for Lab 1 - Getting Started with SoC	MAYNOR BALLINA
15:30	Lab 1 with tutors	
16:00	Coffee Break	
16:15	Lab 1 with tutors	