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Joint ICTP-IAEA School on Detector Signal Processing and Machine Learning for Scientific Instrumentation and Reconfigurable Computing | (smr 4110)

Tuesday 28 October 2025

- Kastler Lecture Hall (AGH) (09:00-18:00)

time	title	presenter
09:00	Semiconductors and Collaboration: Building Pathways for Innovation and Talent	RAZAIDI BIN HUSSIN
09:45	SoC: A Practical Implementation Overview on Intelligent Neurosystems	SAWAL HAMID ALI
10:15	Coffee Break	
10:45	Introduction to AI-Enabled Semantic and Goal-Oriented Communications for the Internet of Things	DEJAN VUKOBRATOVIC
11:45	Hardware Description Language for Reconfigurable Instrumentation	CRISTIAN SISTERNA
12:30	Lunch Break	
14:00	Hardware-Software Co-Design	CRISTIAN SISTERNA
14:45	Guide for Lab 2 - Reconfigurable Instrumentation on SoC-FPGA	MAYNOR BALLINA
15:00	Lab 2 with tutors	
16:00	Coffee Break	
16:15	Lab 2 with tutors	