Joint ICTP-IAEA School on Detector Signal Processing and Machine
Learning for Scientific Instrumentation and Reconfigurable Computing |
(smr 4110)

Contribution ID : **132**   Type : **not specified**

Lab 5.1: Integrating ML Models with HLS4ML for Hardware Synthesis

Friday, 31 October 2025 15:00 (1:00)

Content

Laboratory: Lecturers and Lab Tutors

Summary

Session Classification :   not yet classified