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Joint ICTP-IAEA School on Detector Signal Processing and Machine Learning for Scientific Instrumentation and Reconfigurable Computing | (smr 4110)

Monday 03 November 2025

- Kastler Lecture Hall (AGH) (09:00-18:00)

time	title	presenter
09:00	The Open Standard RISC-V Architecture - Part I	FERNANDO RINCON
10:00	Coffee Break	
10:30	The Open Standard RISC-V Architecture - Part II	FERNANDO RINCON
11:30	Digital Pulse Processing for Isotope Identification	MLADEN BOGOVAC
12:30	Lunch Break	
14:00	Pulse Acquisition and Detector Characterization	MLADEN BOGOVAC
14:45	Guide for Lab 6: Pulse Acquisition and Detector Characterization	LUIS GARCIA
15:00	Lab 6 with tutors	
16:00	Coffee Break	
16:15	Lab 6 with tutors	