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Joint ICTP-IAEA School on Detector Signal Processing and Machine Learning for Scientific Instrumentation and Reconfigurable Computing | (smr 4110)

Wednesday 05 November 2025

- Kastler Lecture Hall (AGH) (09:00-18:00)

time	title	presenter
09:00	Advanced SoC-FPGA Topics	CRISTIAN SISTERNA
10:00	Group Photo	
10:05	Coffee Break	
10:30	Embedded Linux on SoC-FPGA	FABIAN CASTANO
11:30	Handling High Event Rates and Pile-Up - Part I	MLADEN BOGOVAC
12:30	Lunch break	
14:00	Handling High Event Rates and Pile-Up - Part II	MLADEN BOGOVAC
14:30	Guide for Final Project - Part I	LUIS GARCIA
14:45	Project Lab with Tutors	
16:00	Coffee break	
16:15	Project Lab with tutors	