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School on Applied AI for Sustainable Development | (smr 4210)

Wednesday 25 March 2026

Machine Learning and Model Compression on FPGA-Based Heterogeneous Devices - Kastler Lecture Hall (AGH) (09:00-17:45)

Speakers: Romina MOLINA (ICTP, Italy), Maynor Giovanni BALLINA ESCOBAR (ICTP, Italy), Maria Liz CRESPO (ICTP, Italy)

time	title	presenter
09:00	Fundamentals of Applied Machine Learning	
09:45	Model Optimization and Compression Techniques	
10:30	Coffee Break	
11:00	Lab 1: Training Machine Learning Models	
11:00	Lab 2: Applying Model Compression Techniques	
12:30	Lunch Break	
14:00	Using FPGAs to Accelerate Machine Learning Algorithms	
14:45	HyperFPGA: Heterogeneous Computing on MPSoC-FPGA	
15:30	Coffee Break	
15:45	Lab 3: Deploying Machine Learning Models on Heterogeneous MPSoC-FPGA Devices	