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# Regional Workshop and Conference on Advanced Reconfigurable Instrumentation for Scientific Applications | (smr 2816)

## Monday 21 November 2016

### Monday, 21 November 2016 - (09:00-18:30)

time	title	presenter
09:00	VHDL for Implementing Digital Systems into FPGAs	SISTERNA, Cristian
11:00	Coffee break	
11:30	VHDL for Implementing Digital Systems into FPGAs	SISTERNA, Cristian
12:30	Lunch break	
14:00	SoC design based on Zynq device. Programmable Logic (PL)	MEMON, Tayab Din
16:00	Coffee break	
16:30	Zinq design. Programmable Logic. (Lab Tutorial)	MEMON, Tayab Din MANNATUNGA, Kasun Sameera