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Regional Workshop and Conference on Advanced Reconfigurable Instrumentation for Scientific Applications | (smr 2816)

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Tuesday, 22 November 2016 - (09:00-18:30)

time	title	presenter
09:00	Programmable SoC Architecture	SISTERNA, Cristian
10:00	Zynq Architecture: Processing System and Programmable Logic	SISTERNA, Cristian
11:00	Coffee break	
11:30	Zynq Architecture: Processing System and Programmable Logic	SISTERNA, Cristian
12:30	Lunch break	
14:00	Introduction to AXI Interface	SISTERNA, Cristian
15:00	Serial communication interface with FPGA	NANDHA KUMAR, Thulasiraman
16:00	Coffee break	
16:30	Serial communication interface with FPGA	NANDHA KUMAR, Thulasiraman