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Advanced Workshop on FPGA-based Systems-On-Chip for Scientific Instrumentation and Reconfigurable Computing | (smr 3249)

Monday 03 December 2018

- Adriatico Guest House - Giambiagi Lecture Hall (09:00-18:30)

time	title	presenter
09:00	High Level Synthesis (HLS) I	F. RINCON CALLE
10:00	Coffee break	
10:30	High Level Synthesis (HLS) II	F. RINCON CALLE
11:30	Lab Tutorial: HLS and IDE Integration I	F. RINCON CALLE
12:30	Lunch break	
14:00	Lab: HLS and IDE Integration II	TUTORS
15:00	Lab Tutorial: A PyQT GUI for a SoC design (cont)	J.A. DE LA TORRE
16:00	Coffee break	
16:30	Lab Tutorial: from HLS to a PyQT GUI with TCP/IP communication	J.A. DE LA TORRE