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Advanced Workshop on FPGA-based Systems-On-Chip for Scientific Instrumentation and Reconfigurable Computing | (smr 3249)

Tuesday 04 December 2018

- Adriatico Guest House - Giambiagi Lecture Hall (09:00-18:30)

time	title	presenter
09:00	The Era of All Programmable SoC: challenges for instrumentation and supercomputing I	N. ABDALLAH
10:00	Coffee break	
10:30	The Era of All Programmable SoC: challenges for instrumentation and supercomputing II	N. ABDALLAH
11:30	AXI Custom IP	C. SISTERNA
12:30	Lunch break	
14:00	TCP/IP communication: The LwIP library	F. RINCON CALLE
15:00	Lab Tutorial: Custom AXI-Slave IP Core	C. SOSA PAEZ
16:00	Coffee break	
16:30	Lab activities with Tutors	