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Advanced Workshop on Modern FPGA Based Technology for Scientific Computing | (smr 3289)

Friday 17 May 2019

- Giambiagi Lecture Hall (AGH) (09:00-18:30)

time	title	presenter
09:00	The RISK V architecture	NIZAR ABDALLAH
10:30	Coffee break	
11:00	The RISK V architecture (cont.)	NIZAR ABDALLAH
12:30	Lunch break	
14:00	From Advanced Instrumentation Towards Supercomputing	ANDRES CICUTTIN
15:30	Coffee break	
16:00	Lab Tutorial: Data transfer with DMA	RODRIGO MELO
17:00	Lab activities with tutors	