

Table of contents

| | |
|------------------------------|---|
| Monday 25 January 2021 | 1 |
|------------------------------|---|

Joint ICTP-IAEA School on FPGA-based SoC and its Applications for Nuclear and Related Instrumentation | (smr 3562)

Monday 25 January 2021

Session 1 - Online (11:00-13:30)

Chair: Maria Liz CRESPO (ICTP)

The time zone is CET.

You can choose your time zone (in place of of Europe/Rome) on the top right of the page and the programme time will be modified accordingly.

| time | title | presenter |
|-------|---|--|
| 11:00 | Welcome to the School | MLADEN BOGOVAC (IAEA, AUSTRIA), FRANCOIS FOULON (IAEA, AUSTRIA), ANDRES CICUTTIN (ICTP), MARIA LIZ CRESPO (ICTP) |
| 11:30 | School Outline | MARIA LIZ CRESPO |
| 12:00 | Brief introduction from participants | |
| 12:30 | FPGA and System on Programmable Chip Technology | CRISTIAN SISTERNA |