

Table of contents

Monday 25 January 2021	1
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Joint ICTP-IAEA School on FPGA-based SoC and its Applications for Nuclear and Related Instrumentation | (smr 3562)

Monday 25 January 2021

Session 2 - Online (14:30-16:00)

Chair: Andres CICUTTIN (ICTP)

time	title	presenter
14:30	FPGA and System on Programmable Chip Technology (cont.)	CRISTIAN SISTERNA
15:30	Questions & Answers (Session 2)	CRISTIAN SISTERNA