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Joint ICTP-IAEA School on FPGA-based SoC and its Applications for Nuclear and Related Instrumentation | (smr 3562)

Tuesday 26 January 2021

Session 3 - Online (11:00-13:10)

Chair: Andres CICUTTIN (ICTP)

time	title	presenter
11:00	Digital arithmetic and numerical representations	PIROUZ BAZARGAN SABET
11:50	Floating point operators: Square Root and Division	PIROUZ BAZARGAN SABET
12:40	Questions & Answers (Session 3)	PIROUZ BAZARGAN SABET