

Table of contents

Tuesday 26 January 2021	1
-------------------------------	---

Joint ICTP-IAEA School on FPGA-based SoC and its Applications for Nuclear and Related Instrumentation | (smr 3562)

Tuesday 26 January 2021

Session 4 - Online (14:10-16:30)

Chair: Maria Liz CRESPO (ICTP)

time	title	presenter
14:10	System on Chip (SoC) Architecture and Design Methodology	CRISTIAN SISTERNA
15:50	Guide for Lab 1: Hello World	CRISTIAN SISTERNA
16:10	Questions & Answers (Session 4)	CRISTIAN SISTERNA