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Joint ICTP-IAEA School on FPGA-based SoC and its Applications for Nuclear and Related Instrumentation | (smr 3562)

Thursday 28 January 2021

Session 7 - Online (10:50-13:00)

Chair: Maria Liz CRESPO (ICTP)

time	title	presenter
10:50	Fundamentals of VHDL (Hardware Description Language)	RODRIGO ALEJANDRO MELO
12:30	Questions & Answers (Session 7)	RODRIGO ALEJANDRO MELO