

Table of contents

Thursday 28 January 2021	1
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Joint ICTP-IAEA School on FPGA-based SoC and its Applications for Nuclear and Related Instrumentation | (smr 3562)

Thursday 28 January 2021

Session 8 - Online (13:50-16:00)

Chair: Maria Liz CRESPO (ICTP)

time	title	presenter
13:50	High Level Synthesis (HLS)	FERNANDO RINCON CALLE
15:30	Questions & Answers (Session 8)	FERNANDO RINCON CALLE