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Joint ICTP-IAEA School on FPGA-based SoC and its Applications for Nuclear and Related Instrumentation | (smr 3562)

Friday 29 January 2021

Session 9 - Online (10:50-13:00)

Chair: Andres CICUTTIN (ICTP)

time	title	presenter
10:50	FPGAs computing just right thanks to application-specific arithmetic	FLORENT DE DINECHIN
11:40	The FloPoCo arithmetic core generator	FLORENT DE DINECHIN
12:30	Questions & Answers (Session 9)	FLORENT DE DINECHIN