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Joint ICTP-IAEA School on FPGA-based SoC and its Applications for Nuclear and Related Instrumentation | (smr 3562) / Programme

Friday 29 January 2021

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Friday 29 January 2021

Session 10 - Online (13:50-16:00)

Chair: Andres CICUTTIN (ICTP)

time	title	presenter
13:50	Design Methodology for SoC	CRISTIAN SISTERNA
15:30	Questions & Answers (Session 10)	CRISTIAN SISTERNA

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