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Joint ICTP-IAEA School on FPGA-based SoC and its Applications for Nuclear and Related Instrumentation | (smr 3562)

Thursday 04 February 2021

Session 17 - Online (11:00-13:00)

Chair: Bruno VALINOTI (*Instituto Nacional de Tecnología Industrial, Argentina*)

time	title	presenter
11:00	Accurate depth estimation using HLS	JULIO DONDO
12:40	Questions & Answers (Session 17)	JULIO DONDO