Joint ICTP-IAEA School on FPGA-based SoC and its Applications for Nuclear and Related Instrumentation | (smr 3562)

Contribution ID : 563

Type : not specified

VHDL for Custom IP

Thursday, 4 February 2021 14:00 (1:30)

Content

Summary

Presenter(s) :RODRIGO MELO (Instituto Nacional de Tecnologia Industrial, Argentina)Session Classification :Session 18: Laboratory Activities