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Joint ICTP-IAEA School on FPGA-based SoC and its Applications for Nuclear and Related Instrumentation | (smr 3562)

Thursday 04 February 2021

Session 18: Laboratory Activities - Online (14:00-17:00)

Chair: Maria Liz CRESPO (ICTP)

time	title	presenter
14:00	VHDL for Custom IP	RODRIGO MELO
15:30	Guide for Lab 5: Custom IP	RODRIGO MELO
16:00	Lab Activities with tutors	LUIS GARCIA ORDONEZ (UNIVERSIDAD DE SAN CARLOS DE GUATEMALA, GUATEMALA), WERNER FLORIAN (UNIVERSIDAD DE SAN CARLOS DE GUATEMALA, GUATEMALA), BRUNO VALINOTI (INSTITUTO NACIONAL DE TECNOLOGÍA INDUSTRIAL, ARGENTINA), ROMINA MOLINA (NATIONAL UNIVERSITY OF SAN LUIS, ARGENTINA), RODRIGO MELO (INSTITUTO NACIONAL DE TECNOLOGIA INDUSTRIAL, ARGENTINA), CRISTIAN SISTERNA (UNIVERSITY OF SAN JUAN, ARGENTINA)