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Joint ICTP-IAEA School on FPGA-based SoC and its Applications for Nuclear and Related Instrumentation | (smr 3562)

Friday 05 February 2021

Session 19 - Online (11:00-13:10)

Chair: Andres CICUTTIN (ICTP)

time	title	presenter
11:00	FPGA for the Acceleration of Machine Learning Algorithms	ROMINA MOLINA
11:50	High Level Synthesis for Machine Learning (HLS4ML)	ROMINA MOLINA
12:50	Questions & Answers (Session 19)	ROMINA MOLINA