Joint ICTP-IAEA School on FPGA-based SoC and its Applications for Nuclear and Related Instrumentation | (smr 3562)

Contribution ID: 592 Type: not specified

Lab activities with tutors: How to create a PL design and LED blink on the FPGA-based hardware platform

Wednesday, 10 February 2021 14:30 (2:30)

Content

Summary

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