

Table of contents

Wednesday 10 February 2021	1
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Joint ICTP-IAEA School on FPGA-based SoC and its Applications for Nuclear and Related Instrumentation | (smr 3562)

Wednesday 10 February 2021

Session 26: Laboratory Activities - Online (14:00-17:00)

Chairs: Francois FOULON (IAEA, Austria), Mladen BOGOVAC (IAEA, Austria)

time	title	presenter
14:00	FPGA-based Hardware Platform for Ionization Radiation Measurement	MLADEN BOGOVAC (IAEA, AUSTRIA), VLADIMIR RAJOVIC (UNIVERSITY OF BELGRADE, SERBIA)
14:30	Lab activities with tutors: How to create a PL design and LED blink on the FPGA-based hardware platform	MLADEN BOGOVAC (IAEA, AUSTRIA), VLADIMIR RAJOVIC (UNIVERSITY OF BELGRADE, SERBIA), NIKOLA JOVALEKIC (TTTECH COMPUTERTECHNIK AG, AUSTRIA)