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Joint ICTP-IAEA School on FPGA-based SoC and its Applications for Nuclear and Related Instrumentation | (smr 3562)

Thursday 11 February 2021

Session 28: Laboratory Activities - Online (14:00-17:00)

Chairs: Francois FOULON (IAEA, Austria), Mladen BOGOVAC (IAEA, Austria)

time	title	presenter
14:00	Lab activities with tutors: Embedded processor design on the FPGA-based hardware platform (part II)	MLADEN BOGOVAC VLADIMIR RAJOVIC NIKOLA JOVALEKIC