

Table of contents

Friday 19 February 2021	1
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Joint ICTP-IAEA School on FPGA-based SoC and its Applications for Nuclear and Related Instrumentation | (smr 3562)

Friday 19 February 2021

Project presentations from participants groups - Online (11:00-13:00)

Chair: Maria Liz CRESPO (ICTP)

time	title	presenter
11:00	Final Lab Project Presentation	NASIR AHMED
11:06	“Project” Presentation	OLAWALE AKINWALE
11:12	ICTP-IAEA Project	ABDULWAHHAB AL-RUBAYE
11:18	Final project implementation	MAYNOR BALLINA ESCOBAR
11:24	Personal reflection on the workshop	RAJIV BISHWOKARMA
11:30	Project presentation	MARIJA CHAUSHEVSKA
11:36	Final presentation	CRISTIAN LÓPEZ
11:42	Final project	LUCIO MARTINEZ GARBINO
11:48	Digital Pulse Processor based on SoC-FPGA for Particle Detectors - ICTP guided Project	OSCAR OLAYA GUTIERREZ
11:54	Final project presentation	ALI OLFATEH
12:00	Final presentation	MUTHTHANTHIRIGE DILUM RUKSHAN PERERA
12:06	Training experience	HARIYANTI SALEH MOHD
12:12	ICTP-IAEA project presentation	GISELE SONFACK
12:18	My Take Away	GEETIKA SRIVASTAVA
12:24	ICTP School of SoPC	JOSE JUAN SUAREZ VARGAS
12:30	ICTP Project	SHAMSIAH BINTI SUHAILI
12:36	Final presentation	MARSHALL TABETAH
12:42	Break	
12:45	Participants Awards	